

Computer aided verification

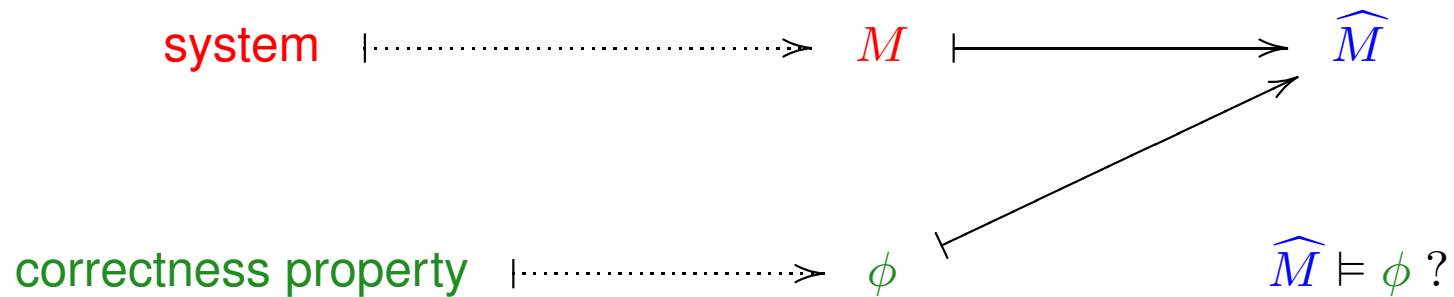
Lecture 8:

Abstraction

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University of Warsaw

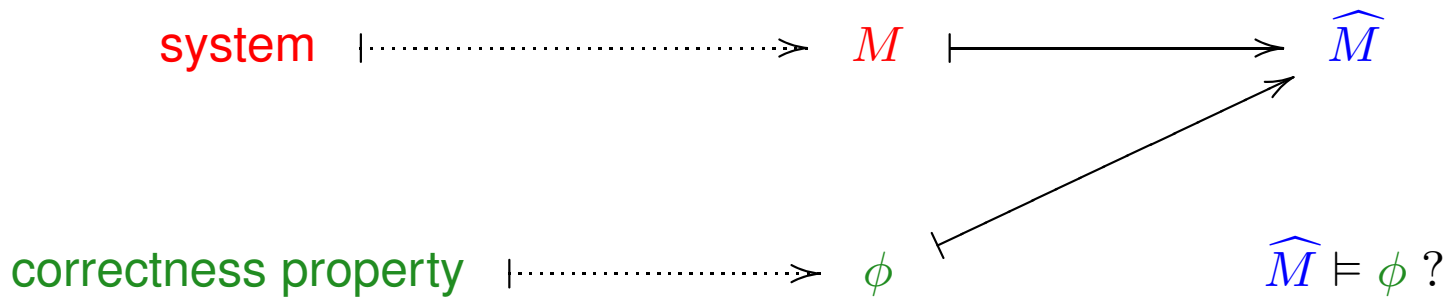
Abstraction

- Model $M = (S, S_0, L, R)$
- **Abstraction** = reduction of the size of the model by removing **irrelevant** details



Abstraction

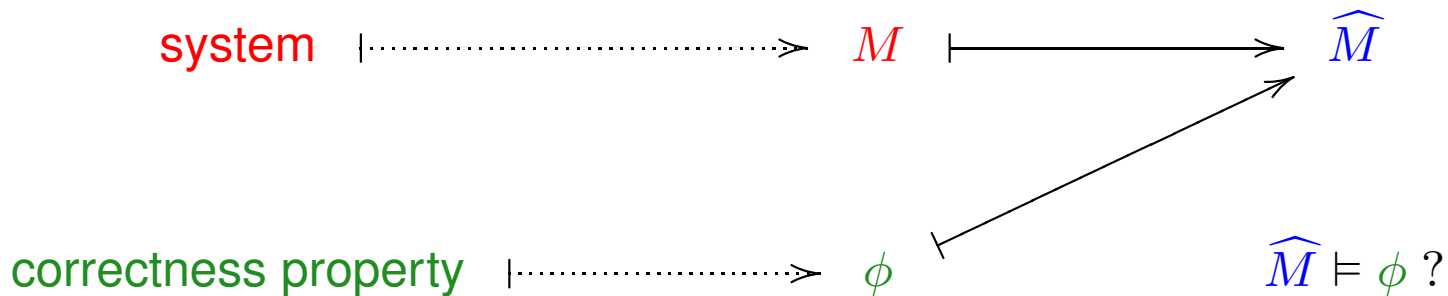
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- We hope that computing \widehat{M} and checking $\widehat{M} \models \phi$ is cheaper than checking $M \models \phi$.

Abstraction

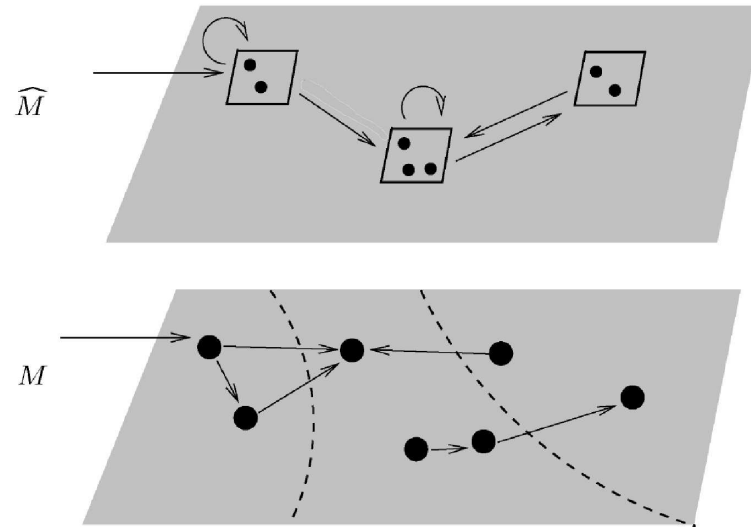
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- We hope that computing \widehat{M} and checking $\widehat{M} \models \phi$ is cheaper than checking $M \models \phi$.
- Abstract model $\widehat{M} = (\widehat{S}, \widehat{S}_0, \widehat{L}, \widehat{R})$ (concrete model $M = (S, S_0, L, R)$)

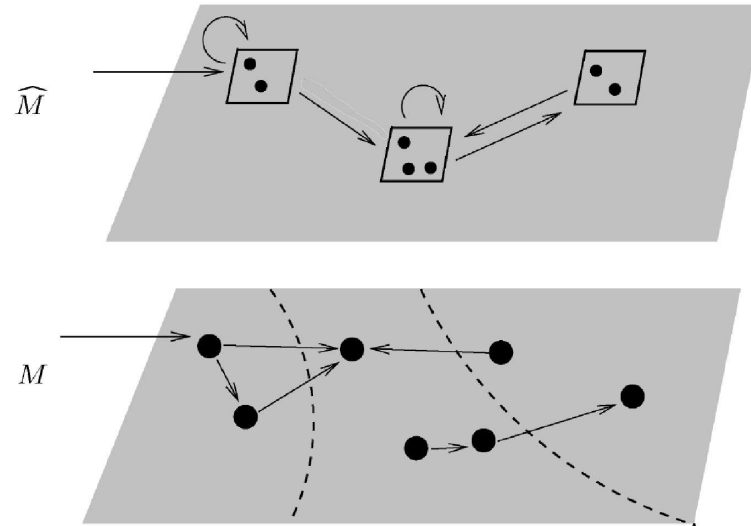
Existential abstraction

Existential abstraction



[Clarke, Grumberg, Jha, Lu, Veith 2003]

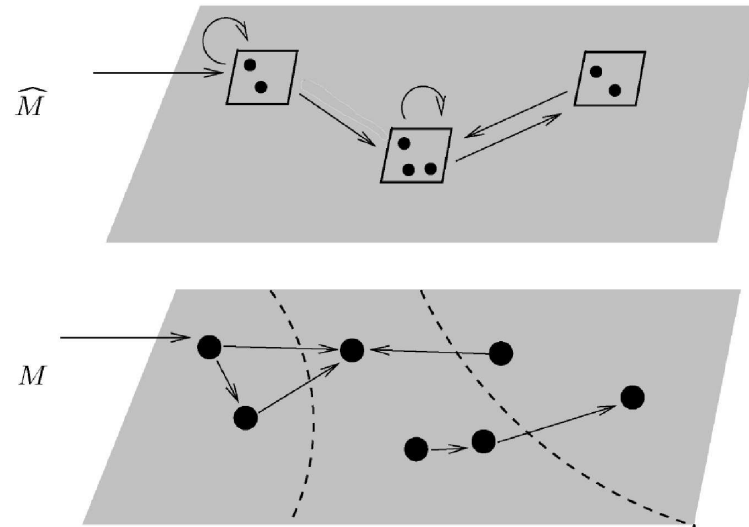
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– Abstraction function $h : M \rightarrow \widehat{M}$ ($h : S \rightarrow \widehat{S}$)

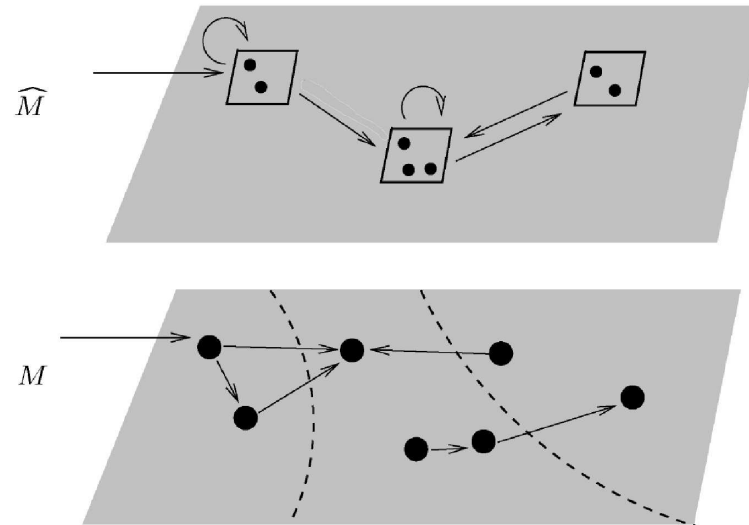
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[Clarke, Grumberg, Jha, Lu, Veith 2003]

- Abstraction function $h : M \rightarrow \widehat{M}$ ($h : S \rightarrow \widehat{S}$)
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 - $\exists s \in S_0. h(s) = \widehat{s} \implies \widehat{s} \in \widehat{S}_0$
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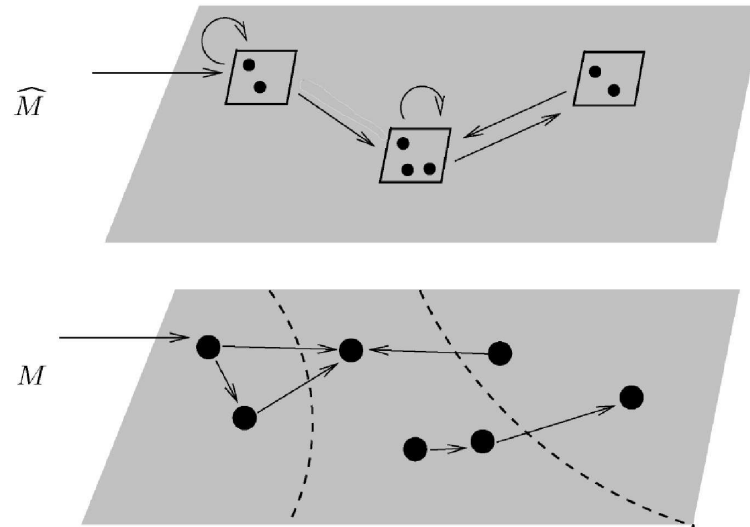
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Minimal existential abstraction



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Hardware: $M = (S, S_0, L, R)$ $V = \{x_1, \dots, x_n\}$ $S = D^n$

R, S_0, L represented by formulas

abstraction applies to [representation of the model](#)

Software: abstraction applies to [program source](#)

running example: predicate abstraction

Predicate abstraction – basic idea

Example: Predicate $x > 0$

$$h_{x>0} : D \rightarrow \{\text{true}, \text{false}\}$$

$$h_{x>0}(d) = \begin{cases} \text{true} & d > 0 \\ \text{false} & d \leq 0 \end{cases}$$

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$$\begin{array}{c} x = 0 \\ \hline x \text{---} \downarrow \\ x = -1 \end{array}$$

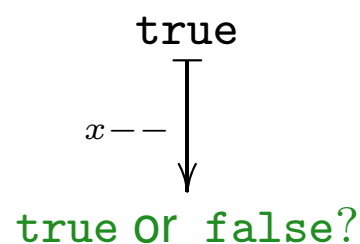
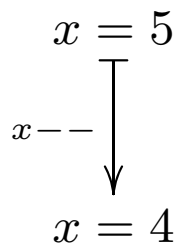
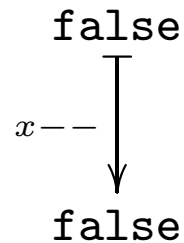
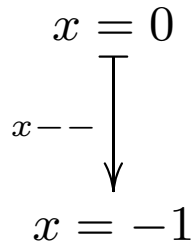
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nondeterminism!

Boolean programs

$$h : D^n \rightarrow \{\text{true}, \text{false}\}^m$$

```
int x, y, z, w;

void foo()
{
[1]  do {
[2]    z = 0;
[3]    x = y;
[4]    if (w){
[5]      x++;
[6]      z = 1;
    }
[7]  } while(x!=y)
[8]  if(z){
[9]    assert(0);
  }
}
```

```
decl b1, b2;
/* b1 stands for predicate (z=0) and
   b2 stands for predicate (x=y) */
void foo()
begin
[1]  do
[2]    b1 := 1;
[3]    b2 := 1;
[4]    if (*)
    begin
[5]      b2 := H(0,b2);
[6]      b1 := 0;
    end
[7]  while(b2)
[8]  if (!b1)
[9]    assert(0);
end

boolean H(e1,e2)
begin
[10] if (e1) then
[11]   return(1);
[12] elsif (e2) then
[13]   return(0);
[14] else
[15]   return(*);
fi
end
```

[T. Ball, S. K. Rajamani 2000]

Correctness via simulation

Simulation game $G_M \hat{M}$

- players: Spoiler, Duplicator

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(assuming one initial state in M and \hat{M})

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- a round
 - Spoiler chooses a transition $s \rightarrow r$ in M (assuming M total)
 - Duplicator answers with a transition $\hat{s} \rightarrow \hat{r}$ in \hat{M} (assuming \hat{M} total)
 - the play continues from the configuration $\langle r, \hat{r} \rangle$

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- configurations: $\langle s, \widehat{s} \rangle$
- initial configuration: $\langle s_0, \widehat{s}_0 \rangle$ (assuming one initial state in M and \widehat{M})
- if $L(s) \neq L(\widehat{s})$, **Spoiler wins**
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 - Spoiler chooses a transition $s \rightarrow r$ in M (assuming M total)
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Question: How to adapt the definition for non-total models?

Simulation game $G_{M \widehat{M}}$

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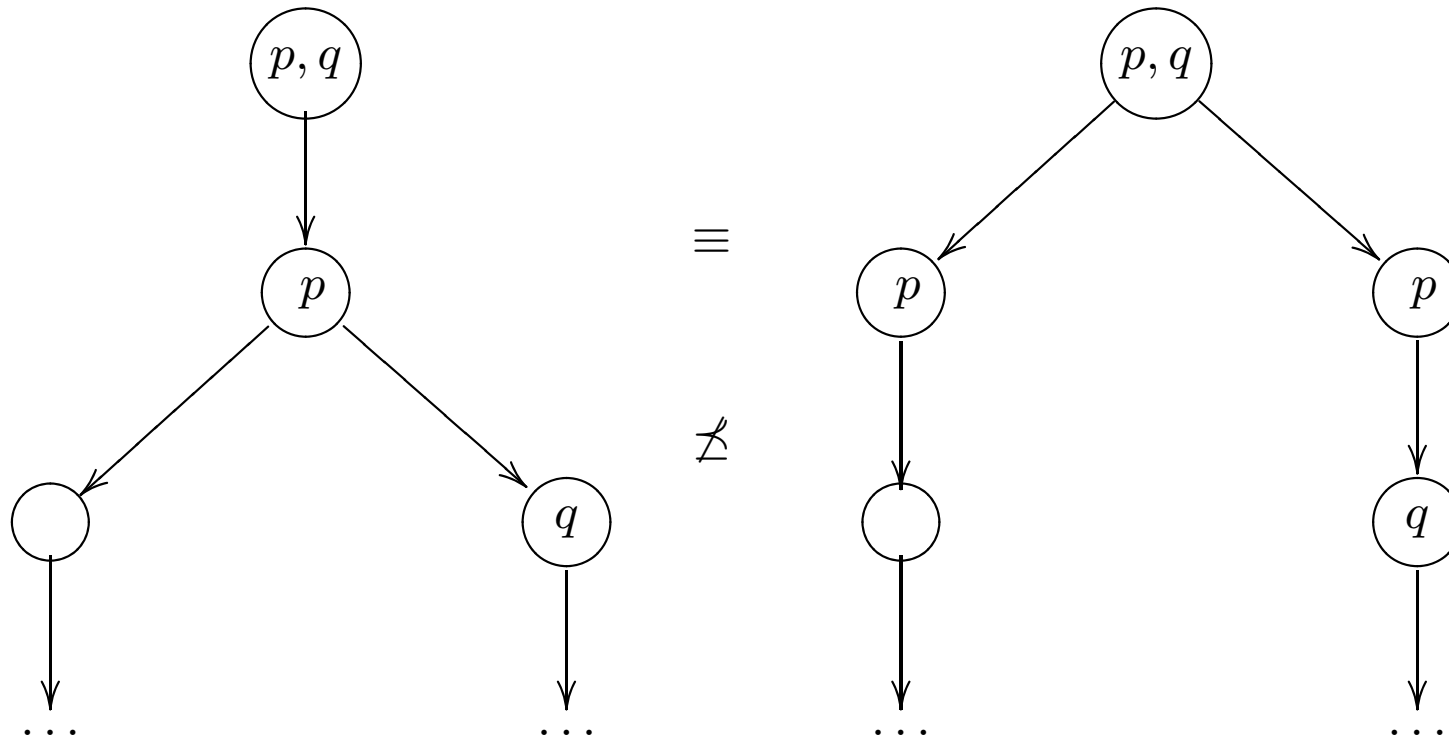
Question: How to adapt the definition for non-total models?

Question: How to adapt the definition for models with more initial states?

Simulation pre-order

Def.: $M \preceq \widehat{M} \iff$ Duplicator has a winning strategy in the game $G_{M \widehat{M}}$

Example:



Correctness via simulation

Simulation preorder is conservative:

Thm.: $M \preceq \widehat{M} \implies (\forall \phi \in \text{ACTL}^*. \widehat{M} \models \phi \implies M \models \phi)$

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Correctness via simulation

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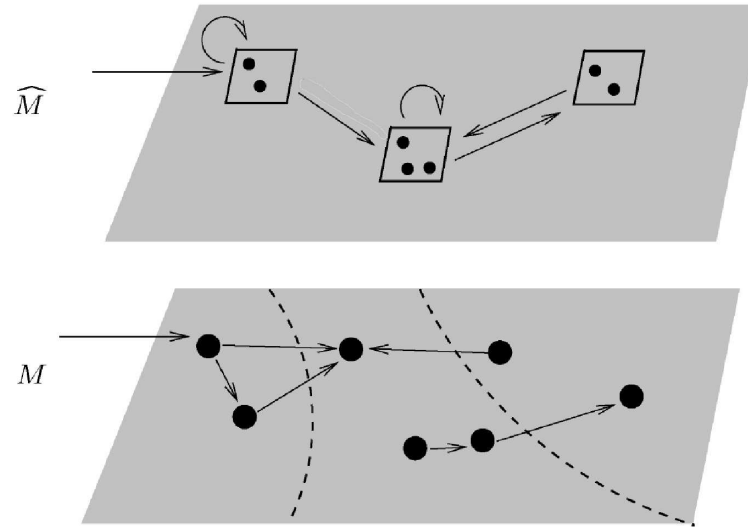
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Def.: Simulation equivalence: $\simeq = \preceq \cap \succeq$

Thm.: $M \simeq \widehat{M} \implies (\forall \phi \in \text{ACTL}^*. \widehat{M} \models \phi \iff M \models \phi)$

Correctness of existential abstraction



[Clarke, Grumberg, Jha, Lu, Veith 2003]

Abstraction function $h : M \rightarrow \widehat{M}$

Thm.: $M \preceq \widehat{M}$, if h preserves the atomic properties

$$h(L(s)) = \widehat{L}(h(s))$$

Fair simulation game $G_{M \hat{M}}$

- players: Spoiler, Duplicator
- configurations: $\langle s, \hat{s} \rangle$
- initial configuration: $\langle s_0, \hat{s}_0 \rangle$
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Fair simulation game $G_{M \widehat{M}}$

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- a round
 - Spoiler chooses a **fair path** $s \rightarrow s_1 \rightarrow s_2 \rightarrow \dots$ (assuming M total)
 - Duplicator answers with a **fair path** $\widehat{s} \rightarrow \widehat{s}_1 \rightarrow \widehat{s}_2 \rightarrow \dots$
 - Spoiler chooses i
 - the play continues from the configuration $\langle s_i, \widehat{s}_i \rangle$

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- if the play is infinite, **Duplicator wins**

Summary

$M \preceq \widehat{M}$	$\forall \phi \in \text{ACTL}^*. \widehat{M} \models \phi \implies M \models \phi$
$M \preceq_F \widehat{M}$	$\forall \phi \in \text{ACTL}^*. \widehat{M} \models_F \phi \implies M \models_F \phi$
$M \simeq \widehat{M}$	$\forall \phi \in \text{ACTL}^*. M \models \phi \iff \widehat{M} \models \phi$
$M \simeq_F \widehat{M}$	$\forall \phi \in \text{ACTL}^*. M \models_F \phi \iff \widehat{M} \models_F \phi$
$M \sim \widehat{M}$	$\forall \phi \in \text{CTL}^*. M \models \phi \iff \widehat{M} \models \phi$
$M \sim_F \widehat{M}$	$\forall \phi \in \text{CTL}^*. M \models_F \phi \iff \widehat{M} \models_F \phi$
$L_\omega(M) \subseteq L_\omega(\widehat{M})$	$\forall \phi \in \text{LTL}. \widehat{M} \models \phi \implies M \models \phi$
$L_\omega(M) = L_\omega(\widehat{M})$	$\forall \phi \in \text{LTL}. M \models \phi \iff \widehat{M} \models \phi$

CEGAR

Counter-example guided abstraction refinement

(1) Compute initial abstraction

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(2) Repeat:

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 - if the result negative – abstract counterexample

Counter-example guided abstraction refinement

(1) Compute initial abstraction

(2) Repeat:

- verify the abstract model
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 - if the result negative – abstract counterexample
- check feasibility of the counterexample
 - if the counterexample feasible – answer **no** and stop

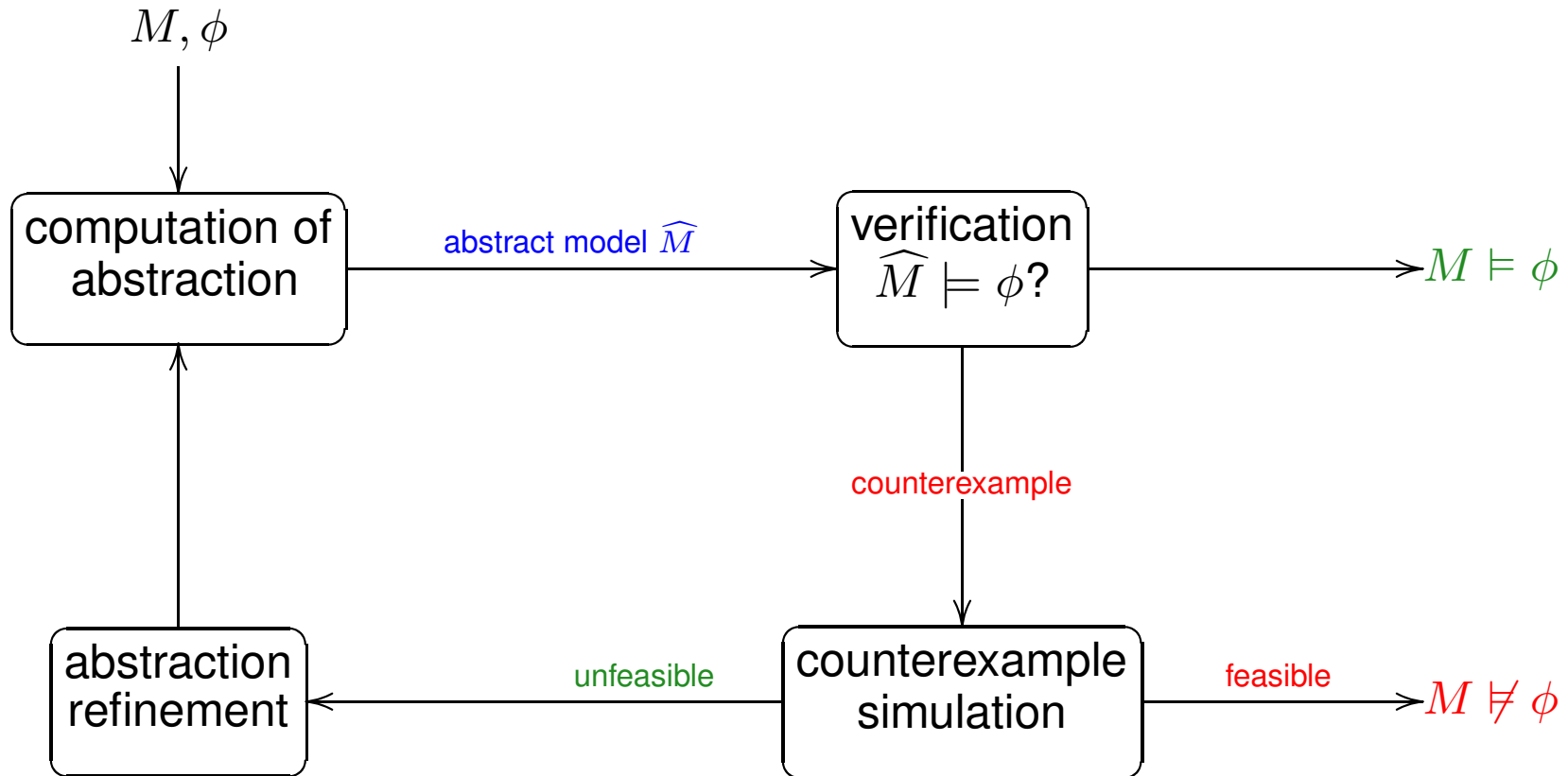
Counter-example guided abstraction refinement

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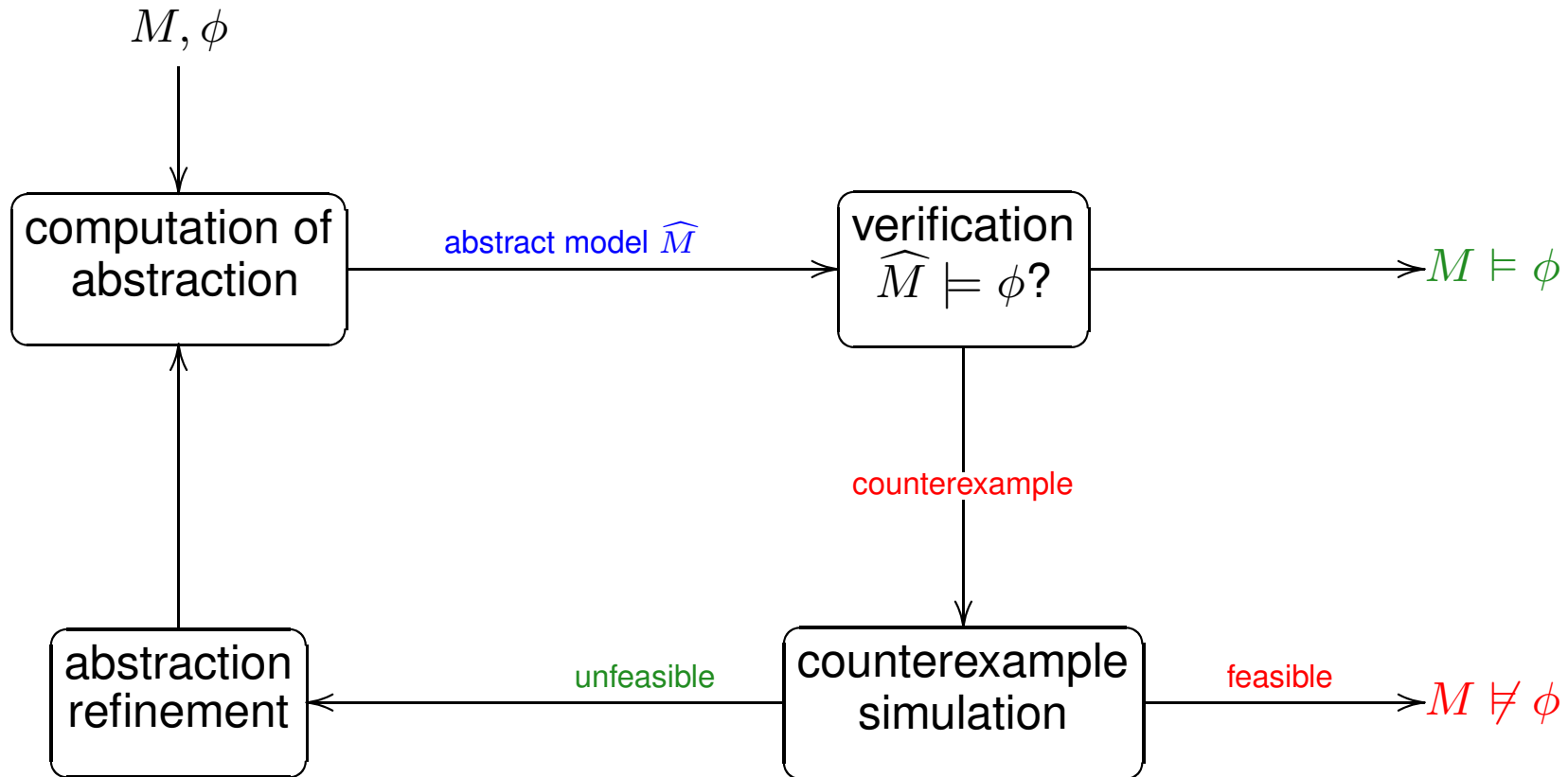
(2) Repeat:

- verify the abstract model
 - if the result positive – answer **yes** and stop
 - if the result negative – abstract counterexample
- check feasibility of the counterexample
 - if the counterexample feasible – answer **no** and stop
- refine the abstraction to **eliminate the unfeasible counterexample**

CEGAR loop

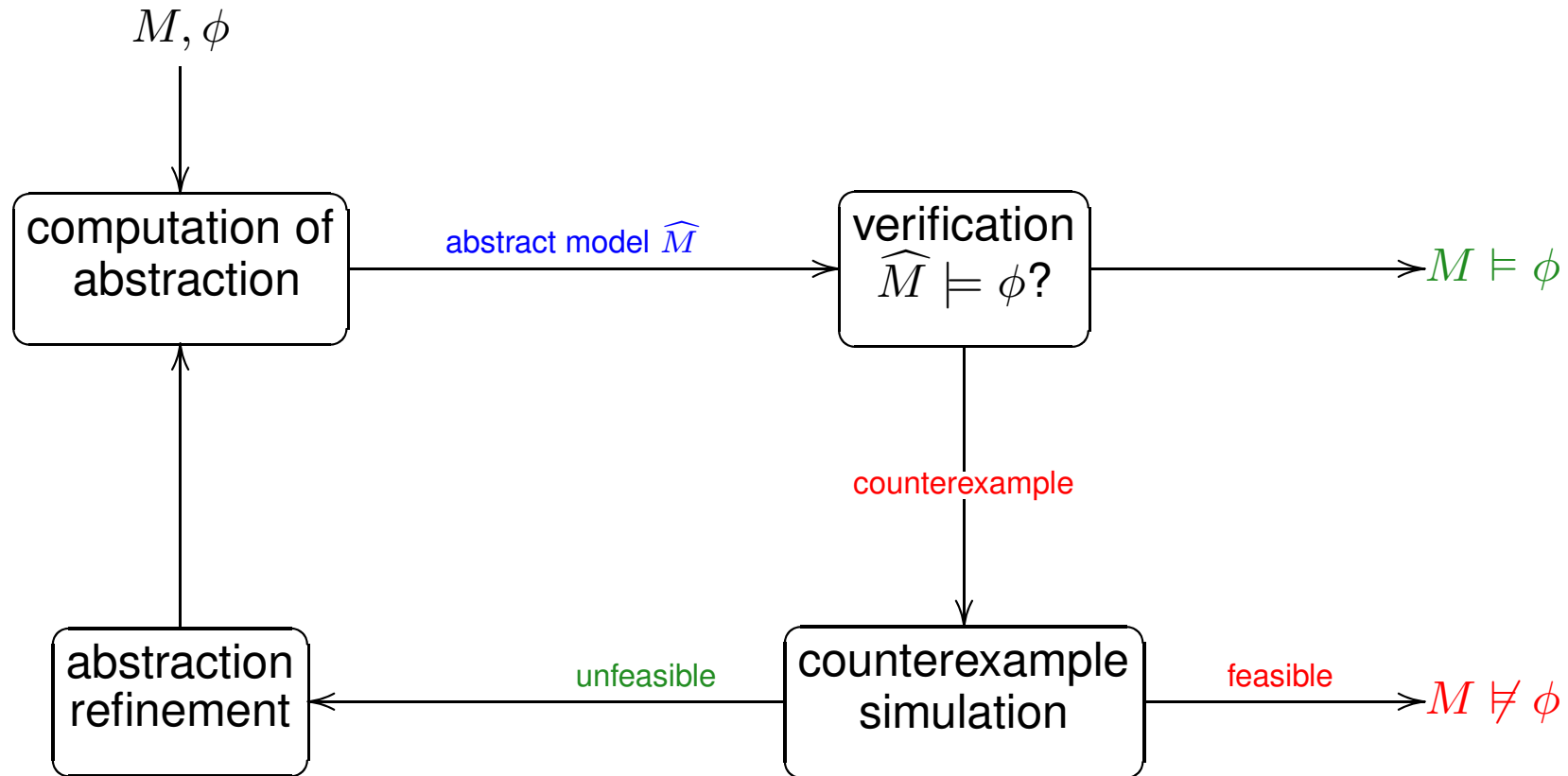


CEGAR loop



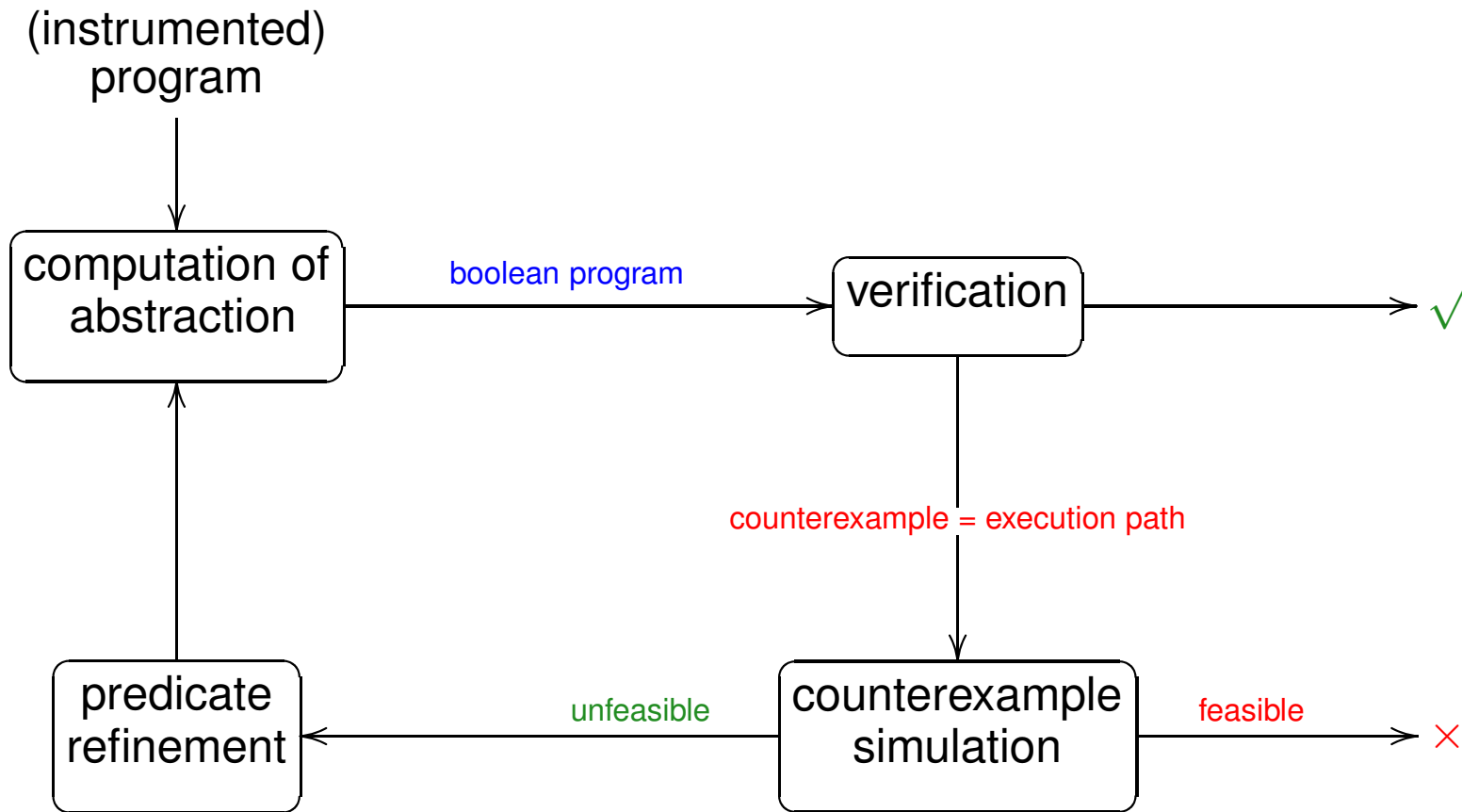
- correctness: whenever CEGAR stops the answer is correct

CEGAR loop



- correctness: whenever CEGAR stops the answer is correct
- termination: complete for finite systems, if minimal abstraction is used

predicate abstraction in CEGAR



CEGAR: initial abstraction

<pre>numUnits: int; level: int; void getUnit() { [1] canEnter: bool := F; [2] if (numUnits = 0) { [3] if (level > 10){ [4] NewUnit(); [5] numUnits := 1; [6] canEnter := T; } } else [7] canEnter := T; [8] if (canEnter) [9] if (numUnits = 0) [10] assert(F); else [11] gotUnit(); } }</pre>	<pre>void getUnit() { [1] ...; [2] if (?) { [3] if (?) { [4] ...; [5] ...; [6] ...; } } else [7] ...; [8] if (?) [9] if (?) [10] ...; else [11] ...; } }</pre>
<i>P</i>	<i>B₁</i>

[T. Ball, S. K. Rajamani 2000]

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CEGAR example

<pre> numUnits: int; level: int; void getUnit() { [1] canEnter: bool := F; [2] if (numUnits = 0) { [3] if (level > 10){ [4] NewUnit(); [5] numUnits := 1; [6] canEnter := T; } } else [7] canEnter := T; [8] if (canEnter) [9] if (numUnits = 0) [10] assert(F); else [11] gotUnit(); } </pre>	<pre> void getUnit() { [1] ...; [2] if (?) { [3] if (?) { [4] ...; [5] ...; [6] ...; } } else [7] ...; [8] if (?) [9] if (?) [10] ...; else [11] ...; } </pre>	<pre> nU0: bool; void getUnit() { [1] ...; [2] if (nU0) { [3] if (?) { [4] ...; [5] nU0:=F; [6] ...; } } else [7] ...; [8] if (?) { [9] if (nU0) [10] ...; else [11] ...; } </pre>	<pre> nU0: bool; void getUnit() { [1] cE: bool :=F; [2] if (nU0) { [3] if (?) { [4] ...; [5] nU0:=F; [6] cE:=T; } } else [7] cE:=T; [8] if (cE) [9] if (nU0) [10] ...; else [11] ...; } </pre>
<i>P</i>	<i>B₁</i>	<i>B₂</i>	<i>B₃</i>

[T. Ball, S. K. Rajamani 2000]

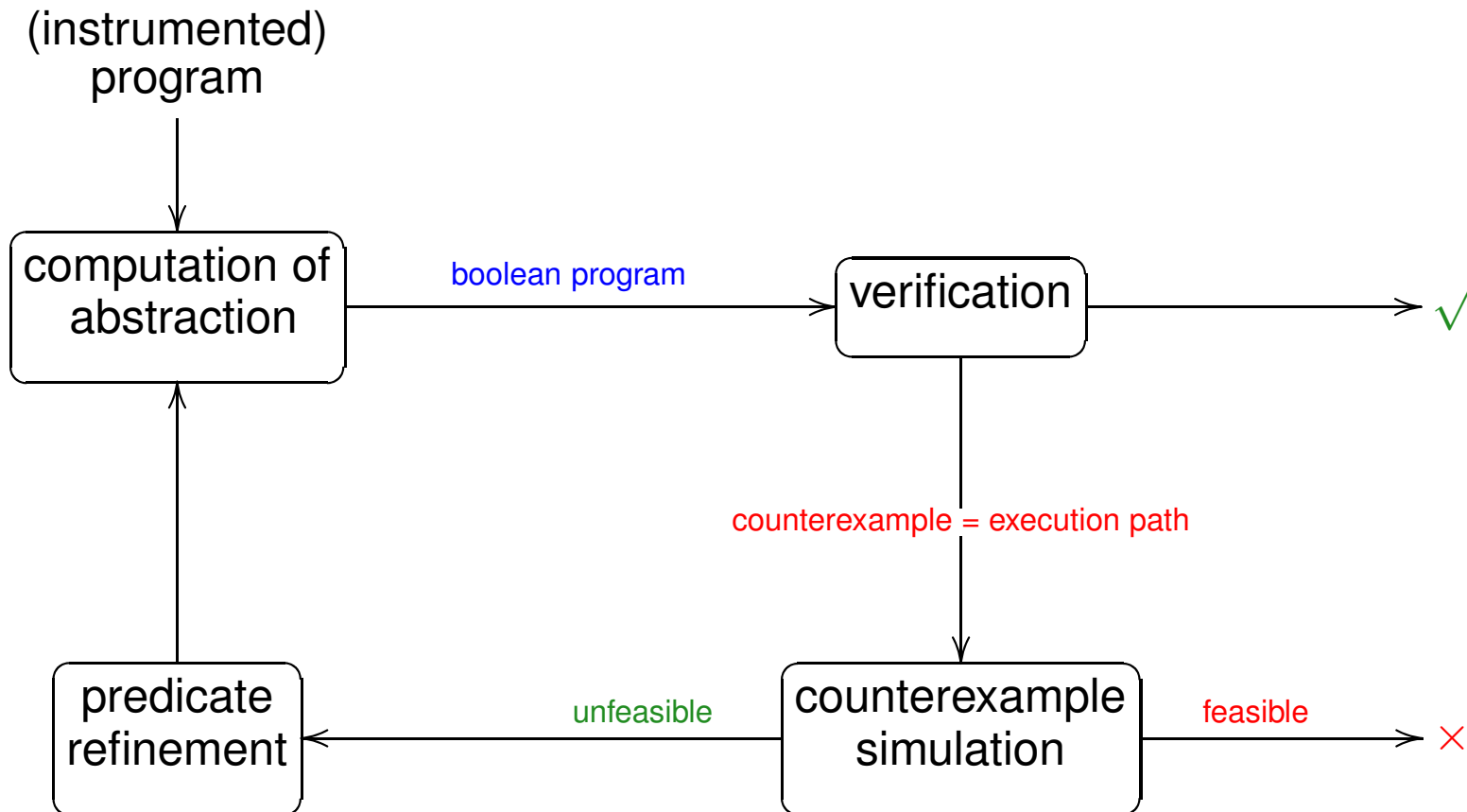
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no reference to variable `level` was needed!

CEGAR: computation of abstraction



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$$h : D^m \rightarrow \{\text{true}, \text{false}\}^n$$

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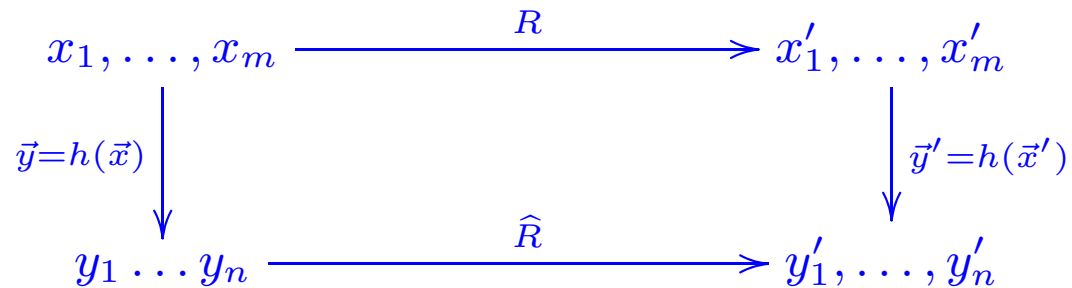
– $\hat{S} = \{\text{true}, \text{false}\}^n$

CEGAR: computation of abstraction

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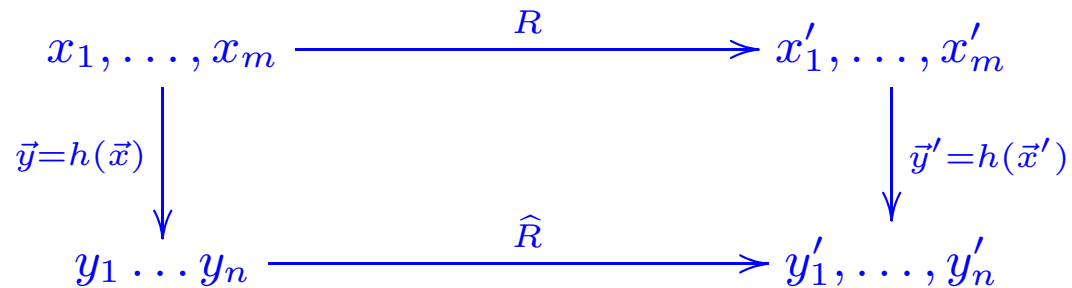


CEGAR: computation of abstraction

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– simple block: $d := e; e++ \quad \mapsto \quad d' = e \wedge e' = e + 1$

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b_1	b_2	b'_1	b'_2
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- calls to a prover or SMT-solver
- non-minimal abstraction (for minimal one, exponential number of calls)
- calls to SAT-solver (assuming boolean encoding of source program)

CEGAR: computation of abstraction

- iteratively call SAT-solver:
 - a satisfying valuation \mapsto a blocking clause
 - backtracking to the highest decision level
 - the procedure stops when unsatisfiable

b_1	b_2	b'_1	b'_2
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
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- control flow: `if (e > 50) {goto l} else {goto l'}`
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- abstract control flow: `if (b1 \vee \neg b2) {goto l} +`
`if (b1 \vee b2) {goto l'}`

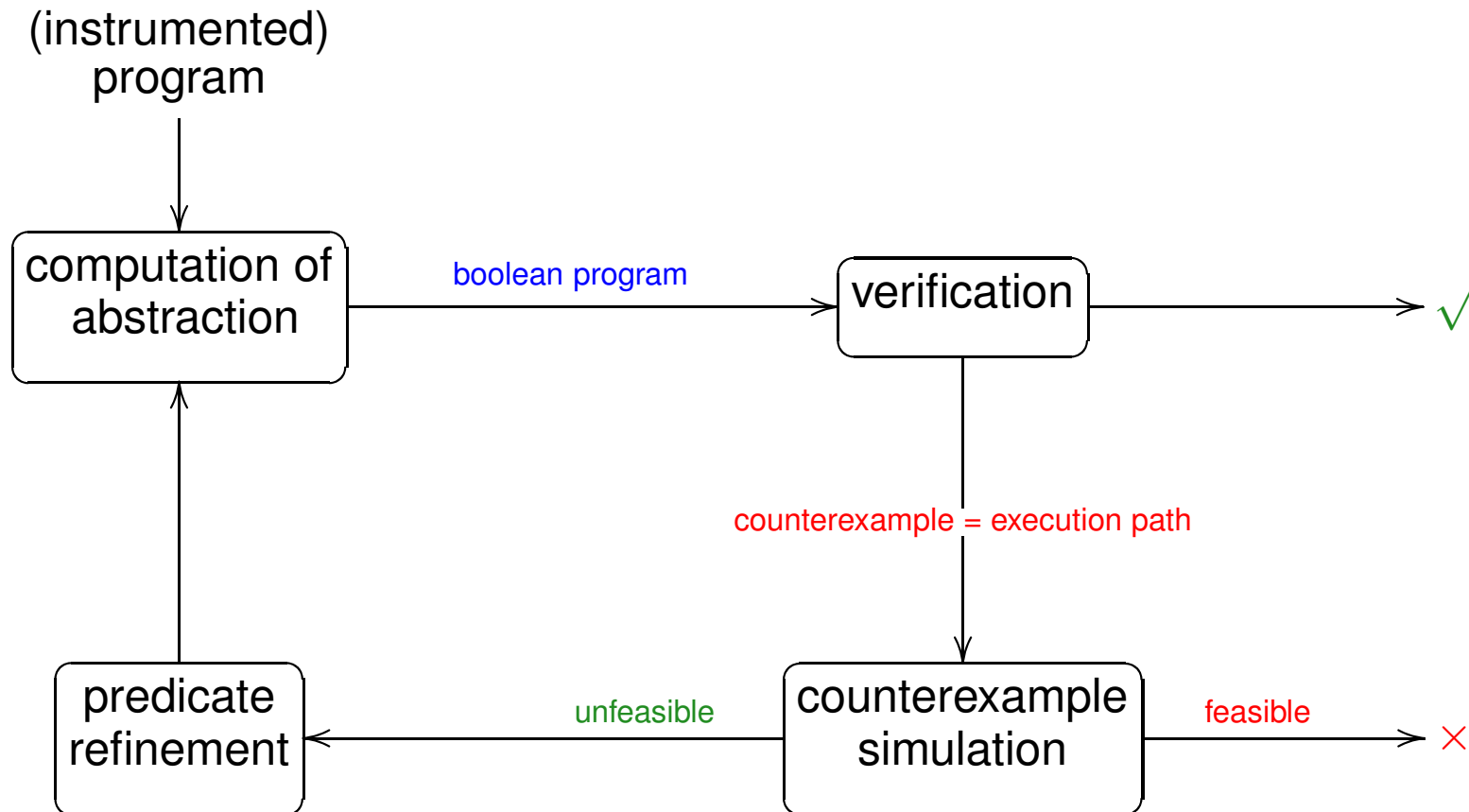
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- abstract guards not necessarily disjoint!
- typically `if (while)` guards are among predicates

CEGAR: verification



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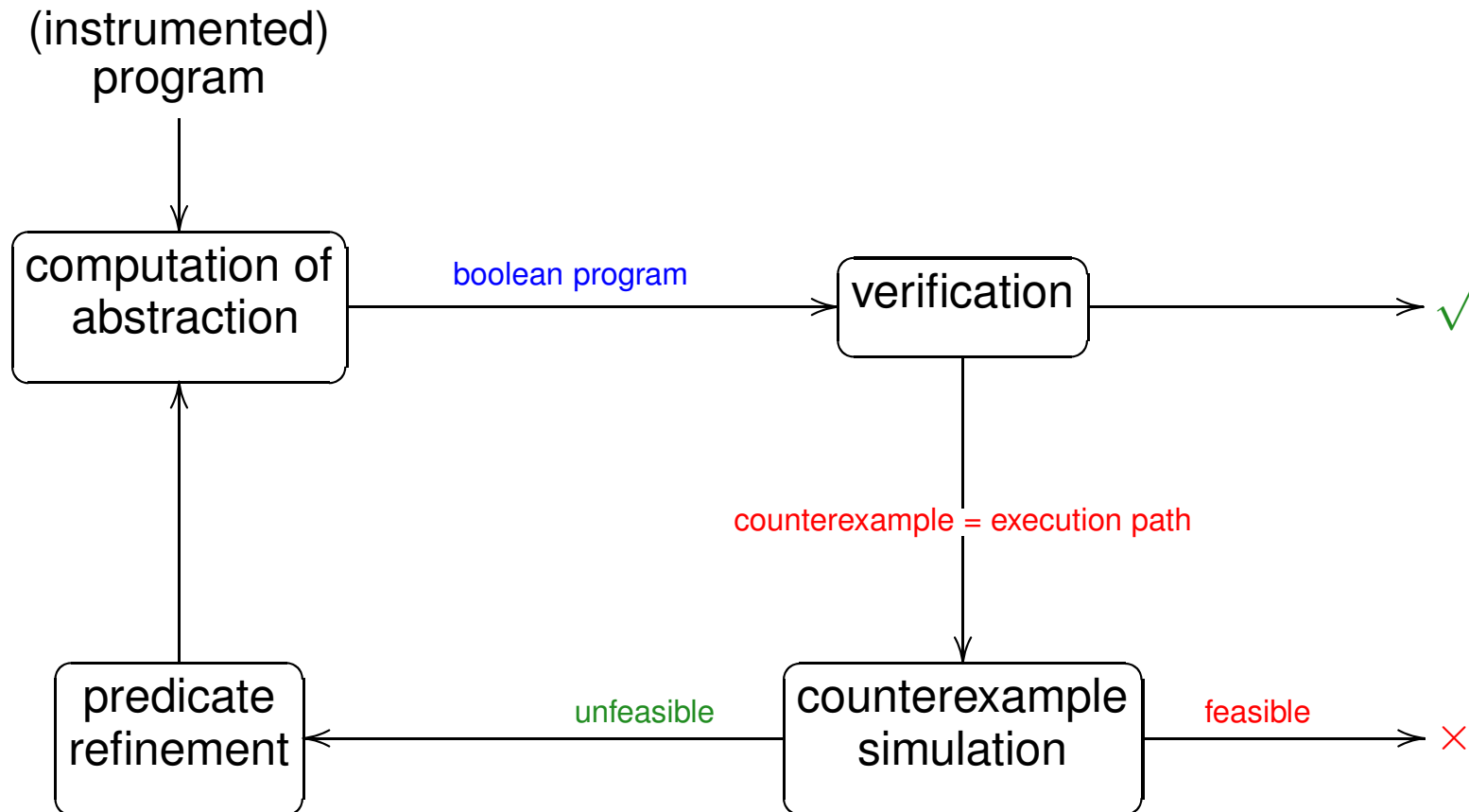
- SMC may be conveniently used (if no recursion nor dynamic allocation)

$$\begin{array}{l} \neg b_1 \wedge b_2 \wedge \neg b'_1 \wedge b'_2 \\ \vee b_1 \wedge \neg b_2 \wedge \neg b'_1 \wedge b'_2 \\ \vee b_1 \wedge b'_1 \wedge \neg b'_2 \\ \vee b_2 \wedge b'_1 \wedge b'_2 \end{array}$$

```
TRANS PC=x -> (!b1 & b2 & !next(b1) & next(b2))
               | ( b1 & !b2 & !next(b1) & next(b2))
               | ( b1           & next(b1) & !next(b2))
               | (           b2 & next(b1) & next(b2))
```

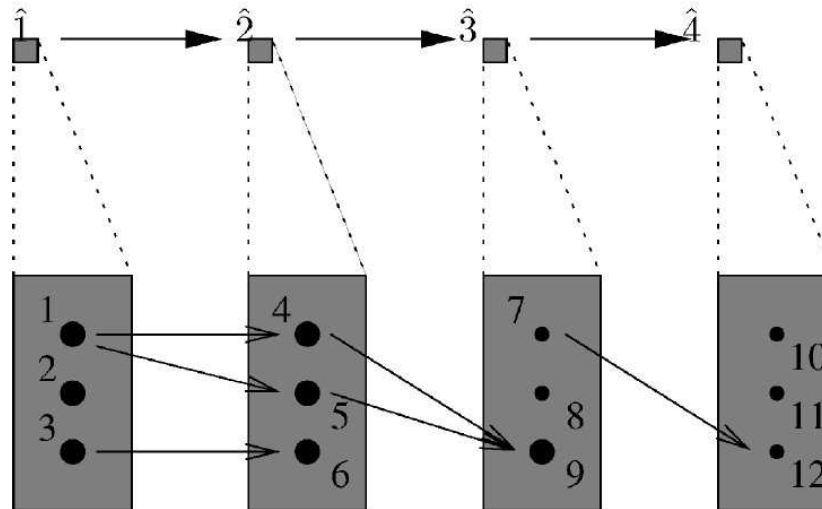
[Clarke, Kroening, Sharygina, Yorav 2004]

CEGAR: counterexample simulation



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abstract counterexample $\hat{1} \hat{2} \hat{3} \hat{4}$



[Clarke, Grumberg, Jha, Lu, Veith 2003]

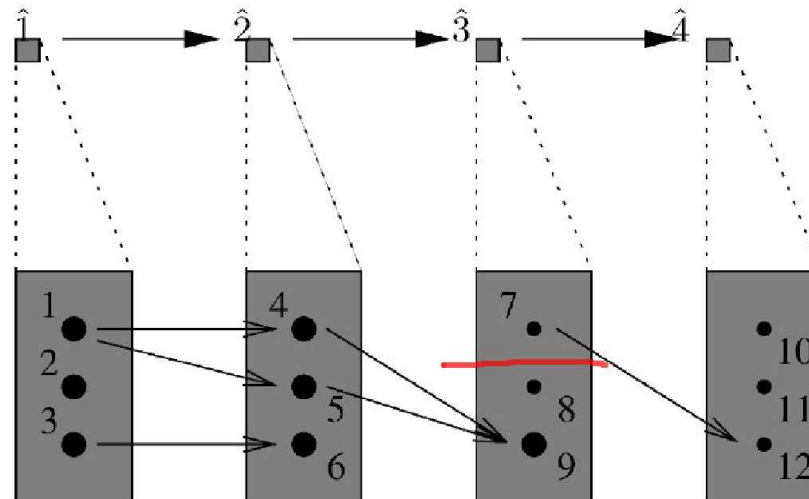
counterexample simulation (strongest post-condition)

$$S_1 := S_0 \cap h^{-1}(\hat{1}) \quad S_i := \vec{R}(S_{i-1}) \cap h^{-1}(\hat{i})$$

by iterative calls to a prover, SMT-solver, or SAT-solver

CEGAR: spurious counterexample

abstract counterexample $\hat{1} \hat{2} \hat{3} \hat{4}$



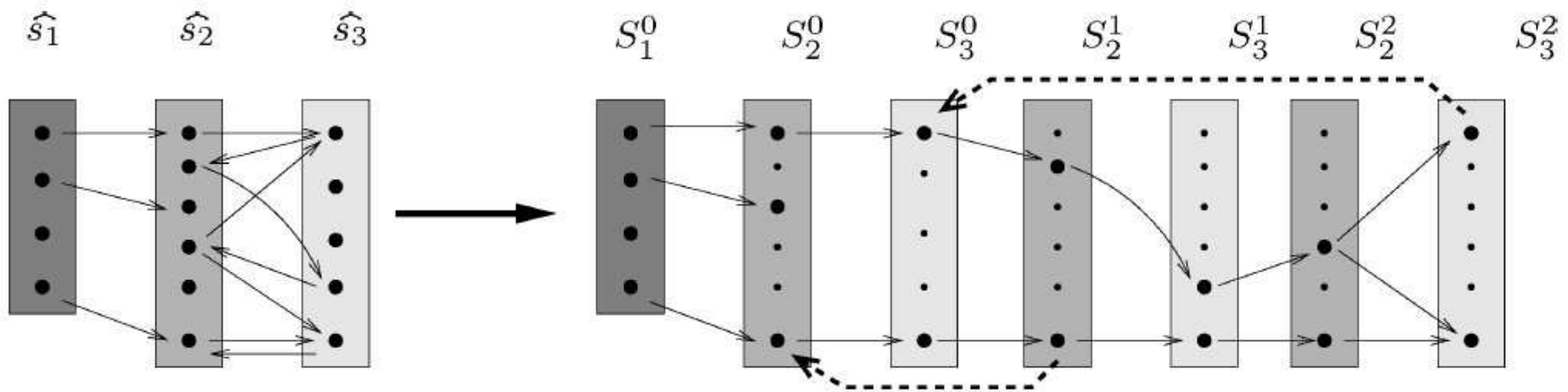
[Clarke, Grumberg, Jha, Lu, Veith 2003]

$$S_4 = \vec{R}(S_3) \cap h^{-1}(\hat{4}) = \emptyset$$

$$S_3 \cap \vec{R}^{-1}(h^{-1}(\hat{4})) = \emptyset$$

CEGAR: infinite counterexample

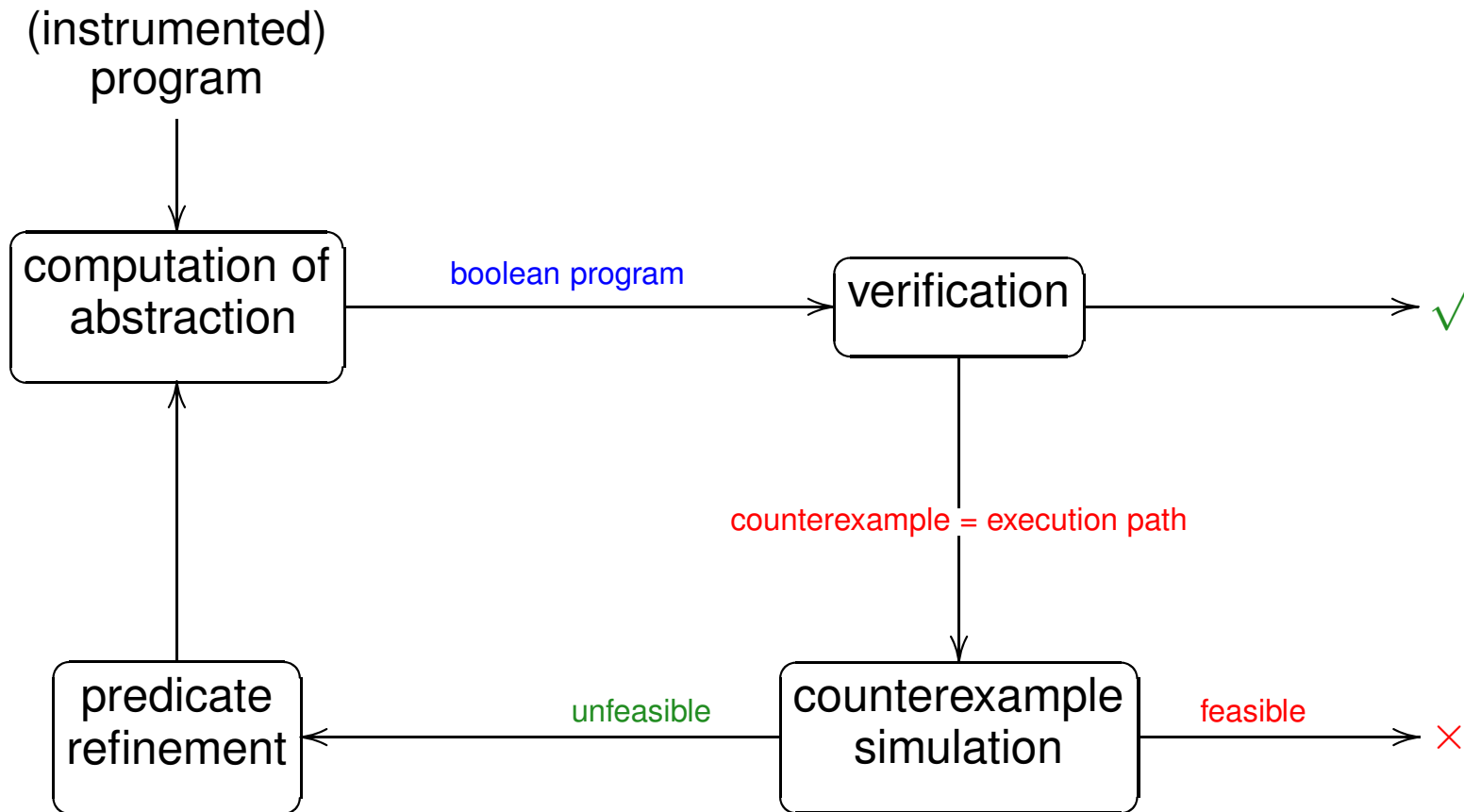
abstract counterexample $\hat{s}_1(\hat{s}_2\hat{s}_3)^\omega$



[Clarke, Grumberg, Jha, Lu, Veith 2003]

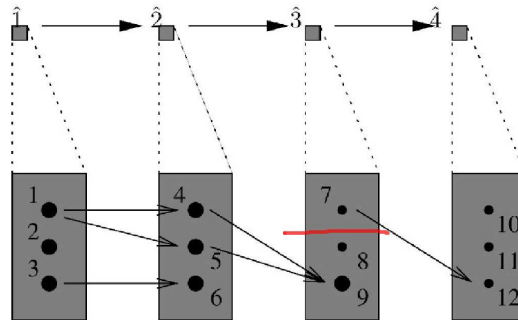
- unfold the loop $\min\{\text{size}(\hat{s}_2), \text{size}(\hat{s}_3)\}$ times

CEGAR: predicate refinement



aim: elimination of the spurious counterexample

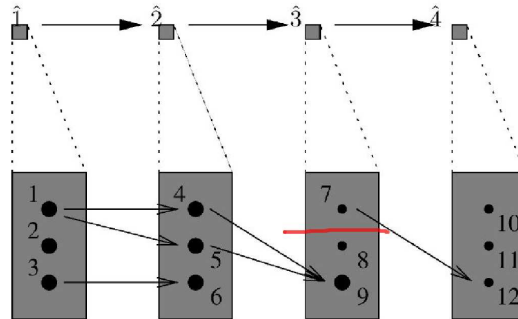
CEGAR: predicate refinement



– spurious counterexample:

$$R(\vec{x}_1, \vec{x}_2) \wedge R(\vec{x}_2, \vec{x}_3) \wedge \dots \wedge R(\vec{x}_{n-1}, \vec{x}_n) \quad \text{unsatisfiable}$$

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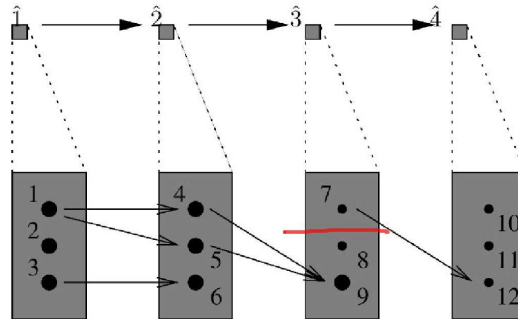
- Craig interpolation: exists a formula $\phi(\vec{x}_i)$ such that

- $\phi(\vec{x}_i)$ only uses variables x_i

- $R(\vec{x}_1, \vec{x}_2) \wedge \dots \wedge R(\vec{x}_{n-1}, \vec{x}_i) \implies \phi$

- $\phi \wedge R(\vec{x}_i, \vec{x}_{i+1}) \wedge \dots \wedge R(\vec{x}_{n-1}, \vec{x}_n)$ unsatisfiable

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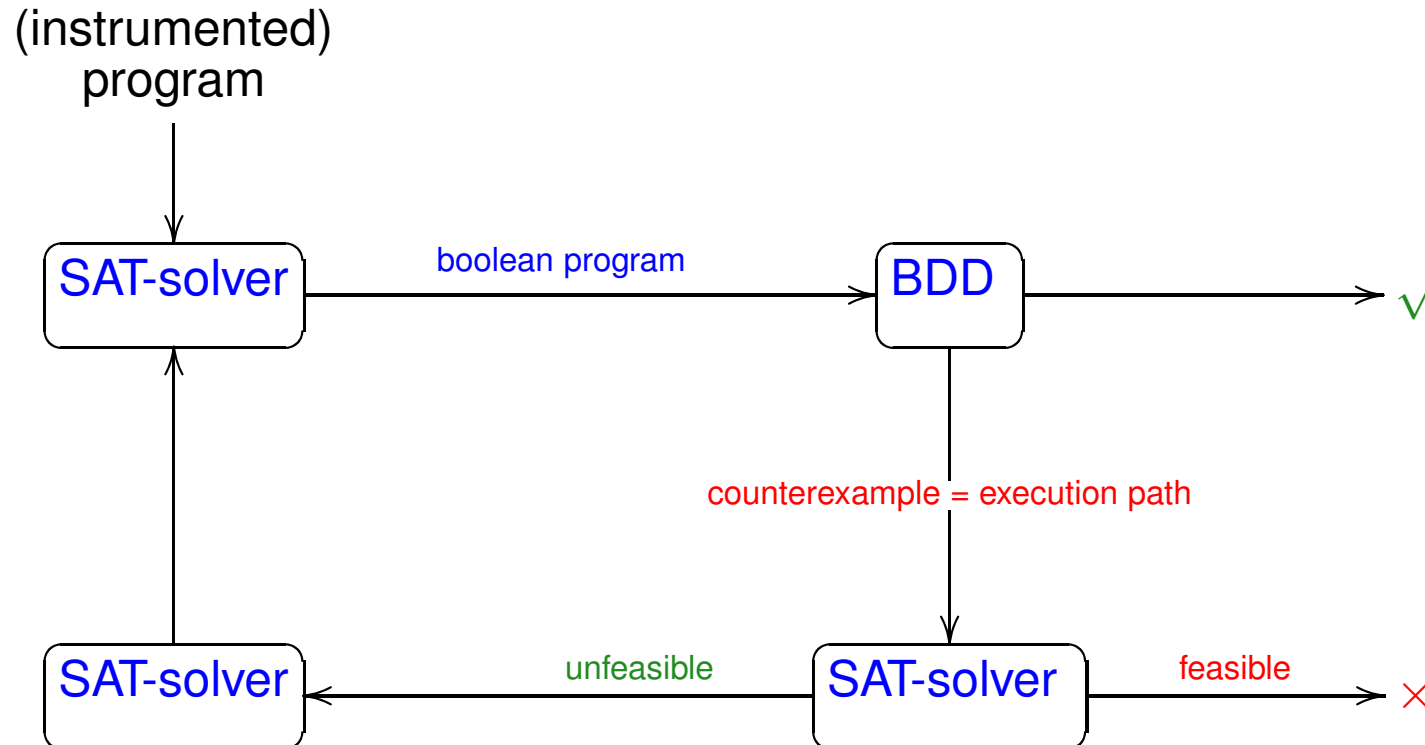
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- For propositional logic, extract Craig interpolant from SAT-solver output

CEGAR: BDD + SAT



Software verification success story

- 85% of system crashes of Windows XP caused by bugs in third-party kernel-level device drivers (2003)
- one of reasons is the complexity of the Windows drivers API
- SLAM: automatically checks device drivers for certain correctness properties with respect to the Windows device drivers API
- now part of Windows Driver Development Kit, a toolset for drivers developers